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end-of-chapter problems included in the book Microelectronic Circuits, International Seventh Edition by Adel S. Sedra and Kenneth C. Smith. Most of the solutions are new; however, I have used and/or adapted some of the solutions from the ISM of the International Sixth Edition. Credit for these goes to the problem solvers listed therein.

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Microelectronic Circuits | 6th Edition. Using the topology of Fig. P6.142, design an amplifier to operate between a 2-k source and a 2-k load with a gain of 40 V/V. The power supply available is 15 V. Use an emitter current of approximately 2 mA and a current of about one-tenth of that in the voltage divider that feeds the base, with the dc voltage at the base about onethird of the supply.

[Using the topology of Fig. P6.142, design an amplifier to](#)

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